

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10007099-1

SERIAL NO.

09/704,176

APPLICANT

Blain D. Gaith r et al

FILING DATE

10/31/00

GROUP

2186

JCS32 U.S. PTO
09/704176

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
SE	1A	6,049,847	Apr 11, 2000	Vogt et al	710	130
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

SE	1Q	Scott E. Crawford & Ronald F. DeMara; "Cache Coherence In A Multiport Memory Environment"; Dept. of Electrical and Computer Engineering; University of Central Florida; IEEE Comp. Soc. Press; 2-6 May 1994; pages 632-642
SE	1R	Michael Carlton, Univeristy of California at Berkeley & Alvin Despain, University of Southern California; "Multiple -Bus Shared-Memory System: Aquarius Project"; Computer Journal; vol. 23, no.6; June 1990; pages 80-83
SE	1S	Julie Jones, Student Member, IEEE and Rog r L. Haggard, Member, IEEE; Department of Electrical and Computer Engineering; Tennessee Technological University; "Hardwar -based Methods For Maintaining Cache Coherency In A Multiprocessor System"; 8-10 March 1998 ; IEEE; pages 549-553

EXAMINER

S. E. Moore

DATE CONSIDERED

12-15-2002